

## REMARKS

The Applicant has amended claims 1 and 4-7. Claims 2, 3 and 8 are canceled. Claims 1 and 4-7 are now pending in the application. For at least the following reasons, it is submitted that this application is in condition for allowance.

### 1. Allowable Subject Matter

The Applicant acknowledges with appreciation the Examiner's indication in the Office Action that claims 3-4 would be allowable if rewritten in independent form including all the limitations of the base claim and any intervening claims. Accordingly, claim 1 has been amended to include the limitations of original claim 3 and intervening claim 2. It is respectfully submitted, therefore, that claim 1, as amended, is now in condition for allowance.

Claim 4 has been amended to change its dependency from claim 2 to claim 1.

Claim 4 depends from claim 1, and is in condition for allowance for at least the reasons that the Examiner considered amended claim 1 allowable.

### 2. Rejection Under Section 112 and Objections to Claims

The rejection of claim 5 under 35 U.S.C 112, second paragraph has been addressed by deleting the improper recitations of the third, fourth, and fifth resistor units, and the impedance matching resistor unit. In addition, the errors noted in item 1 on page 2 of the Official Action has been corrected by appropriate amendments to claims 5-8.

The subject matter of claims 5 and 6 was indicated as allowable on page 5 of the Official Action, subject to overcoming the above noted Section 122 rejection and formal objections.

### 3. Rejection of Claim 7 Under 35 USC 102(b)

The rejection of Claim 7 under 35 U.S.C. 102(b) as being anticipated by Ferianz is respectfully traversed on the grounds that the Ferianz patent neither discloses nor suggests the claimed third *or* fourth resistor units (in combination with the first resistor unit), now positively recited in claim 7, as follows:

Claim 7. (Currently Amended) A line driver, comprising:  
a differential amplifier having an inverting output terminal, a non-inverting output terminal, an inverting input terminal, and a non-inverting input terminal;  
a first input resistor coupled to the non-inverting input terminal;  
a second input resistor coupled to the non-inverting input terminal;  
a first impedance matching resistor coupled to the non-inverting output terminal;  
a second impedance matching resistor coupled to the non-inverting output terminal; and  
a resistive feedback network, having a plurality of resistors in symmetric configuration, wherein the resistive feedback network further includes:  
a first resistor unit including a first resistor connected to the first impedance matching resistor and the inverting input terminal and a second resistor connected to the second impedance matching resistor and the non-inverting input terminal, and one of the following two resistor units comprising:  
a third resistor unit including a fifth resistor connected to the second impedance matching resistor-and the inverting input terminal and a sixth resistor connected to the first impedance matching resistor and the non-inverting input terminal; and  
a fourth resistor unit including a seventh resistor connected to the inverting output terminal and the inverting input terminal and a eighth resistor connected to the non-inverting output terminal and the non-inverting input terminal.

Because the resistive feedback network recited in claim 7, either having the first resistor unit and a third resistor unit, or else having the first resistor unit and a fourth

resistor unit, is not disclosed by Ferianz, withdrawal of the rejection of claim 7 under 35 USC 102(b) is respectfully requested.

For the foregoing reasons, it is respectfully submitted that claims 1 and 4-7 are patentable and that this application is in condition for allowance. Should the Examiner feel that a conference would be helpful in expediting the prosecution of this application, the Examiner is hereby invited to contact the undersigned counsel to arrange for such an interview.

Respectfully submitted,

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